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**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re application of :

M. Ueda

Serial No.: 09/682,188

Filed: August 2, 2001

For: Multiprocessor System, Processor Module For Use Therein, And Task  
Allocation Method For Multiprocessing



Date: September 6, 2001

Group Art Unit: 2185

Examiner:

Docket No.: JP920000046US1

Assistant Commissioner for Patents  
Washington, D.C. 20231

**TRANSMITTAL LETTER**

Sir:

Please find enclosed a listing (PTO Form 1449) of references which may be relevant to the above-referenced application. This listing of references is not a representation that a search has been made, that better references than listed do not exist, or that other references are not applicable. Moreover, this listing does not constitute any admission by applicants or applicants' representative that the information provided herein is necessarily prior art to applicants' invention. Please charge any fee necessary to enter this paper to deposit account 09-0468.

Respectfully submitted,

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P.O. Box 218  
Yorktown Heights, New York 10598

FORM PTO-1449 (Modified)

**LIST OF PATENTS AND PUBLICATIONS  
FOR APPLICANT'S INFORMATION  
DISCLOSURE STATEMENT**

ATTY. DOCKET NO.

JP920000046US1

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APPLICANT:

M. Ueda

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GROUP:

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## REFERENCE DESIGNATION

## U.S. PATENT DOCUMENTS

EXAMINER INITIALS		DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE (IF APPRO.)
	AA	5,713,004	1/27/98	Kimmel et al.	G06F	12/08	
	AB						
	AC						
	AD						
	AE						
	AF						

## FOREIGN PATENT DOCUMENTS

		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION	
							YES	NO
	AG	JPUP5-151064	6/18/93	JP	G06F	12/02		
	AH	JPUP4-321136	11/11/92	JP	G06F	9/46		
	AI							

## OTHER ART (Including Author, Title, Date, Pertinent Pages, etc.)

	AJ	
	AK	

EXAMINER

DATE CONSIDERED

EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.